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(alternating ADJ buffers) AND (@AD<20010125 OR @RLAD<20010125 OR @PRAD<20010125)
alternating AND buffer AND read AND write AND PCI AND MII AND (@AD<20010125 OR @RLAD<20010125 OR @PRAD<20010125)
alternating AND buffer AND read AND write AND PCI AND (@AD<20010125 OR @RLAD<20010125 OR @PRAD<20010125)
alternating AND buffer AND read AND write AND (@AD<20010125 OR @RLAD<20010125 OR @PRAD<20010125 OR
(alternating ADJ buffer) AND PCI AND read AND write AND (@AD<20010125 OR @RLAD<20010125 OR @BDAD<20010175)
(370/419,445,449,450,451,452,458,459,460,463,466,477) OR (395/200.8,860) OR (709/232) OR (710/52,56,104,305,306,310,317)).CCLS. AND buffers AND PCI AND MII AND network AND control AND transmit AND (@AD<20010125 OR @BD AD<20010125)
((370/419,445,449,450,451,452,458,459,460,463,466,477) OR (395/200.8,860) OR (709/232) OR (710/52,56,104,305,306,310,317)).CCLS. AND buffers AND PCI AND network AND control AND transmit AND (30AD < 30010175 OR 30R AD < 30010175 OR
((370/85.2,85.3,85.6,419,463,466,477) OR (395/200.8,860) OR (709/232) OR (710/52,56,104,305,306,310,317)).CCLS. AND buffers AND PCI AND network AND control AND transmit AND (@AD<20010125 OR @RLAD<20010125 OR @PRAD<20010125)
((370/85.2,85.3,85.6,419,463,466,477) OR (395/200.8,860) OR (709/232) OR (710/52,56,104,305,306,310,317)).CCLS. AND (@AD<20010125 OR @RLAD<20010125 OR @PRAD<20010125)
Silididileous AND (@AD<20010125 OK @KLAD<20010125 OK @PRAD<20010125) ((370/85.2,85.3,85.6,419,463,466,477) OR (395/200.8,860)).CCLS. AND (@AD<20010125 OR @RLAD<20010125 OR @PRAD<20010125)
(first AD) buffer) AND (second AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND (second AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND (second AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND (second AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND (second AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND PCI AND network AND control AND transmit AND full AND (first AD) buffer) AND PCI AND (first AD) buffer) AND PCI AND (first AD) buffer)
(First ADJ) buffer) AND (second ADJ) buffer) AND (AND AND AND Control AND transmit AND full AND (Second ADJ) AND
(first ADJ buffer) AND (second ADJ buffer) AND PCI AND network AND control AND transmit AND (@AD<20010125 OR @RLAD<20010125 OR @PRAD<20010125)
buffers AND PCI AND network AND control AND transmit AND (@AD<20010125 OR @RLAD<20010125 OR @BDAD<20010135)
(`606/585').PN. MII AND PCI AND network AND (@AD<20010125 OR @RLAD<20010125 OR @PRAD<20010125)
((370/419)).CCLS. AND (@AD<20010125 OR @RLAD<20010125 OR @PRAD<20010125)